

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: MIYAMOTO, et al.

Serial No.: Not yet assigned

Filed: February 20, 2004

For: SEMICONDUCTOR INTEGRATED-CIRCUIT DEVICE AND METHOD
TO SPEED-UP CMOS CIRCUIT

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97 & 1.98

Mail Stop DD
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 20, 2004

Sir:

In the matter of the above-identified application, applicants are submitting herewith a copy of the documents listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This information disclosure statement is being submitted with the new application.

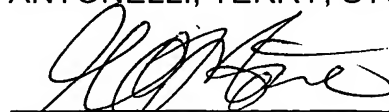
Although some of the documents listed on the attached form equivalent to Form PTO-1449 are not in the English language, the requirement of 37 CFR 1.98(a)(3) for a concise explanation of the relevance is satisfied by the discussion of the documents in the specification, for example, on pages one (1) and two (2).

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus Deposit Account No. 01-2135 (Case: 520.43528X00), and please credit any excess fees to such deposit account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS LLP



Gregory E. Montone
Registration No. 28,141

Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DKT. NO. 520.43528X00	SERIAL NO.
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		APPLICANT MIYAMOTO, et al.	
		FILING DATE February 20, 2004	GROUP

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
AA	6,380,764	04/30/2002	KATOH, et al.	326	93	12/16/1998
AB	2002/0027256	03/07/2002	ISHIBASHI, et al.	257	391	11/16/2001
AC						
AD						
AE						
AF						
AG						
AH						
AI						
AJ						
AK						
AL						

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Abstract	
						Yes	No
AM	11-195976	07/21/1999	JP	H03K	19/0944		X
AN	2001-15704	01/19/2001	JP	H01L	27/10		X
AO							
AP							
AQ							
AR							
AS							
AT							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	AU	
	AV	
	AW	
	AX	
	AY	
	AZ	
Examiner	Date Considered	